

SPECIFICATION FOR DEEP UV LED

3535 UVC LED PKG

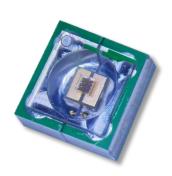
DESCRIPTION

The BR35QF275S01X is a deep UV LED package based on brilliant aluminum reflector with a peak wavelength of 275 nm.

In the case of aluminum, the reflectivity of the UV wavelength band is the best metal.

Moreover, reliability is also excellent because the LED PKG body radiates heat as a whole.

In the case of a transparent quartz glass that protects the UV LED, the application of quartz glass maximizes the light output of the UV LED.



FEATURES

- Brilliant Aluminum SMT package with quartz glass
- Dimension (L x W x H) in mm
 - 3.5 x 3.5 x 1.8
- Forward current: up to 150 mA
- Radiant power (typ.)
 - 10.5 mW at 100 mA
- Viewing Angle (2θ1/2)
 - Typical 125°
- Built in ESD Protection device

APPLICATIONS

- Sterilization
- Disinfection
- Chemical and Biological analysis
- Phototherapy
- Fluorescent Spectroscopy
- Counterfeit Detectors
- Irradiance



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1. PKG Specifications

1) Absolute maximum ratings

[Ta=25℃]

| Parameters | Symbol | Value | Unit |
|-----------------------|--------|-------------|------|
| Forward current | If | 150 | mA |
| Power dissipation | Pd | 1.05 | W |
| Operating temperature | Topr | - 40 ~ +80 | ℃ |
| Storage temperature | Tstg | - 40 ~ +100 | °C |
| Soldering temperature | Tsol | 260*) | ℃ |
| ESD Classification | | Class2**) | |

Notes

2) Electrical and Optical Characteristics

[Ta=25℃]

| Parameter | Symbol | If | Min | Тур | Max | Unit |
|-------------------------------|-------------------|-------|------|------|------|------|
| Peak wavelength | λр | 100mA | 270 | 275 | 280 | nm |
| | | 100mA | 8 | 10.5 | 16 | |
| Radiant Flux | Фе | 120mA | - | 12.2 | - | mW |
| | | 150mA | - | 14.7 | - | |
| Forward voltage | Vf | 100mA | 5.0 | 5.84 | 7.5 | V |
| Full Width at Half Maximum | Δλ | 100mA | 11.0 | | nm | |
| Viewing angle | 2θ1/2 | 100mA | 125 | | deg | |
| Thermal resistance | Rθ _{J-s} | 100mA | 45.0 | | °C/W | |

Notes

- Peak Wavelength(λp) Measurement tolerance is ± 3 nm.
- Radiant Flux(Φ e) Measurement tolerance is $\pm 10\%$.
- Forward Voltage(Vf) Measurement tolerance is ±3%.
- Viewing angle(deg) Measurement tolerance is ±5deg.

^{*)} Recommend JEDEC-J-STD-020D for reflow soldering.

^{**)} The ESD test follows JESD22-A114.



2. Bin Structures

[If=100mA, Ta=25 $^{\circ}$ C]

| Item | Bin | Min | Max | Unit |
|-----------------|-----|-----|-----|------|
| Peak Wavelength | W | 270 | 280 | nm |
| Radiant Flux | R1 | 8 | 10 | |
| | R2 | 10 | 12 | |
| | R3 | 12 | 14 | mw |
| | R4 | 14 | 16 | |
| Forward Voltage | V1 | 5.0 | 5.5 | |
| | V2 | 5.5 | 6.0 | |
| | V3 | 6.0 | 6.5 | V |
| | V4 | 6.5 | 7.0 | |
| | V5 | 7.0 | 7.5 | |

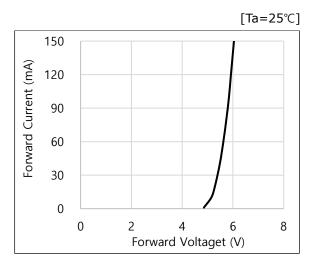
Notes

- Peak Wavelength(λp) Measurement tolerance is ± 3 nm.
- Radiant Flux(Φe) Measurement tolerance is ±10%.
- Forward Voltage(Vf) Measurement tolerance is ±3%.

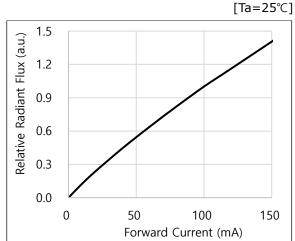


3. Typical Characteristics Graphs

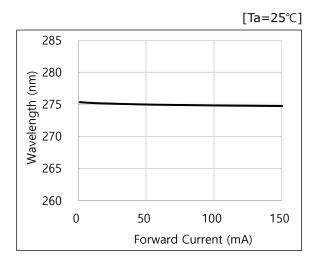
1) Forward Current vs. Forward Voltage



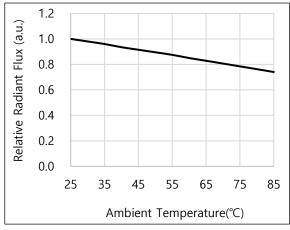
2) Relative Radiant Flux vs. Forward Current



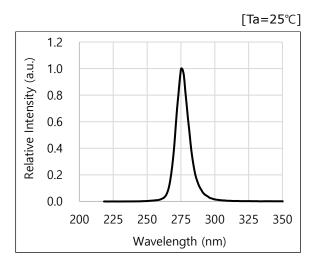
3) Peak Wavelength vs. Forward Current



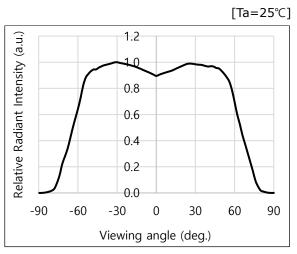
4) Ambient Temp. vs. Relative Radiant Flux



5) Spectrum



6) Typical Spatial Distribution

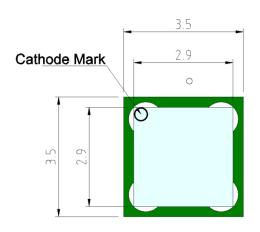




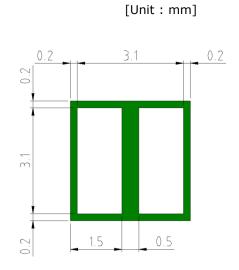
4. Outline Dimensions

1) PKG Dimensions

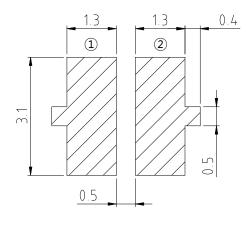
- PKG Dimensions 3.5 x 3.5 x 1.8 (L x W x H)
- Undefined tolerance is ±0.2mm



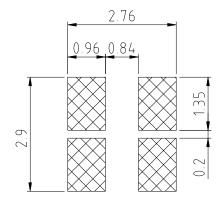




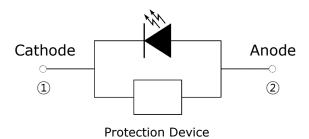
2) Recommended Solder Pad



3) Recommended Stencil Pattern

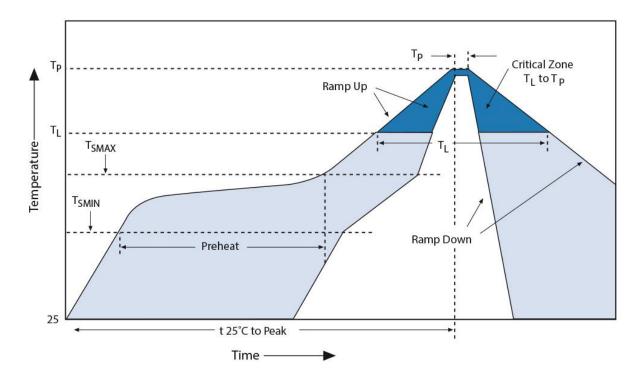


4) Internal Circuit





5. Reflow Soldering Profile



[Classification Reflow Profiles]

| Profile Feature | Pb-free Assembly | Sn-Pb Eutectic Assembly |
|---|------------------------------------|------------------------------------|
| Average ramp-up rate (Ts _{max} to Tp) | 3°C/second max | 3°C/second max |
| Preheat - Temperature Min (Ts _{min}) - Temperature Max (Ts _{max}) - Time (Ts _{min} to Ts _{max}) (ts) | 150 °C 200 °C 60-180 seconds | 100 °C 150 °C 60-120 seconds |
| Time maintain above: - Temperature (T _L) - Time (t _L) | 217 °C 60-150 seconds | 183 °C 60-150 seconds |
| Peak Temperature (Tp) | 260 ℃ | 235 ℃ |
| Time within 5°C of actual Peak Temperature (tp)² | 20-40 seconds | 10-30 seconds |
| Ramp-down Rate | 6 °C/second max | 6 °C/second max |
| Time 25°C to Peak Temperature | 8 minutes max. | 6 minutes max. |

Notes

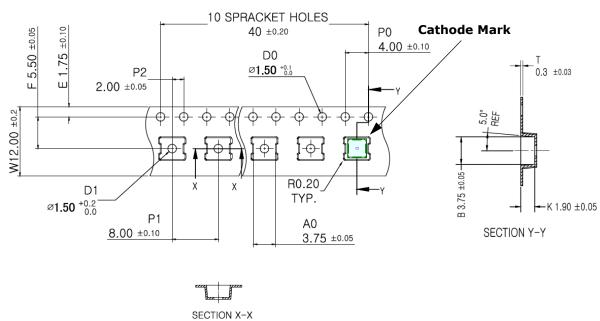
- All temperature refer to topside of the package, measured on the package body surface.
- The LED package is designed to be reflow soldered to a PCB. If dip soldered or hand soldered,
 PEC will not guarantee its reliability.
- Reflow soldering must not be done more than two times.
- When the LED PKG is cooled at the maximum temperature, a rapid temperature fall is not recommended.



6. Taping and Reel packing

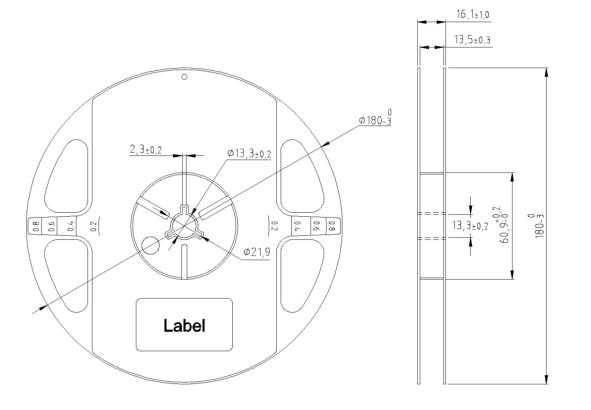
1) Tape

[Unit:mm]



2) Reel

PKG Quantity: 1,000 PCS/Reel [Unit: mm]





7. Precautions on use

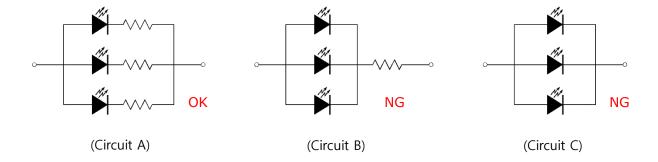
1) Storage condition

- This LED PKG is vacuum-packed in an aluminum bag containing a dehumidifying agent. However, if the storage environment is not good, the LED PKG can absorb moisture.
- When soldering with LED PKG absorb moisture, vaporization of moisture may occur and internal air may expand. This may cause the quartz to peel off or deteriorate its optical properties.
- It is recommended to keep in the environment shown in the table below.

| | Conditions | Temperature | Humidity | Time |
|---------|----------------|------------------------------|------------|--------------------|
| | Before Opening | e Opening 5°C ~ 30°C < 50%RH | | Within 1 Year from |
| Storage | Aluminum Bag | Bag | V 30701411 | the Delivery Date |
| Storage | After Opening | 5℃ ~ 30℃ | < 60%RH | ≤ 672 hours |
| | Aluminum Bag | | | 2 072 110013 |
| | Baking | 65 ± 5℃ | < 10%RH | 10 ~ 24 hours |

2) Circuit design

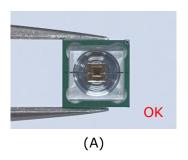
- When designing the circuit by applying the BR series, be sure to not exceed the absolute maximum ratings of each LED.
- For the BR series, it is designed to operate with forward voltage. If a reverse voltage is applied, the LED chip and the Zener diode can be damaged.
- For the current drive method, it is recommended to operate in the constant current mode.
- For Circuit, it is recommended to add additional resistors for each LED, like Circuit A. When designing as Circuit B, C, current deviations may occur due to Vf deviation of LED, which causes defects.

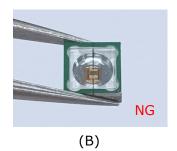


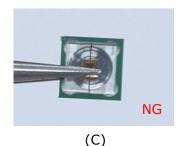


3) Handling precautions

- When handling the LED PKG using a tweezer, handle it in a direction parallel to the electrode separation line as shown in Fig. A below.
- When handling as shown in Fig. B, short-circuit failure may occur due to damage between electrode separation lines.
- When handling as shown in Fig. C, may cause quartz to break or scratch.







- Do not do rapid cooling immediately after soldering. It can cause the failure of the

- Handling the LED PKG by hand may contaminate the LED surface, which may cause the light output to drop.
- Dropping the LED PKG may cause LED damage.

4) ESD (Electrostatic Discharge)

- Use all objects and materials in the workplace for anti-static measures.
- Grounding

LED PKG.

- ① All instruments, JIG, and equipment in the workplace should be grounded and measured once a month by a ground resistance meter.
- ② Workers should wear antistatic clothing and ground through a wrist strap or heel ground. Earth ring should be connected so as not to be shaken when grounding, and check for disconnection every day.
- The working surface of the work table is provided with a conductive mat and grounded.

 Periodically measure and check the conductivity state.
- The work table preferably has a surface resistance of 105 to 109 [Ω /SQ], and the metal work table is not good. (Breakage due to rapid discharge)
- Install conductive tiles or mats and regularly measure and manage grounding resistance and static electricity.
- Partially high static electricity is generated such as rotating body, TV, monitor, JIG. If it is non-conductive, use ionizer or anti-static spray regularly and prevent static electricity.



BR35QF275S01X

- In addition to general cases, use humidity control or partial humidifier to suppress the generation of static electricity.

5) Thermal Management

- Thermal management is closely related to the lifetime of LEDs.
- The temperature of the LED during operation must not exceed the junction temperature (Tj).
- For temperature management of the LED, the thermal resistance of the PCB and the spacing between the LEDs must be considered.
- For PCBs, a Cu based metal PCB is recommended, and if necessary, a heat sink should be attached to manage the LED temperature.

6) Eye Safety

- This UV LED PKG emits high power UV light.
- Exposure to light with strong UV light can cause damage to the human eye and skin.
- Do not look directly or indirectly at UV light.
- If exposure to UV light is unavoidable, the body should be protected by suitable protection devices such as goggles and clothing.
- Attach the following warning labels on products/systems that use UV LEDs.

